Measurement-induced entanglement and teleportation on a noisy quantum processor

> IPMU, U. of Tokyo, September 2024 Pedram Roushan (Google Quantum AI)

space / qubits $\psi_{\rm m}$ time / depth

Measurement-induced entanglement and teleportation \rightarrow Quantum information phases in space-time

Measurement is key in many protocols

A genuine NISQ problem Can noise destabilize phases



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Nature 622 (2023) (arXiv:2303.04792)



Cao, Tilloy, De Luca, SciPost Physics (2019) Skinner, Ruhman, Nahum, PRX (2019), Li, Chen, Fisher, PRB (2018), Chan, Nandkishore, Pretko, Smith, PRB (2019) Challenges in studying monitored circuits



$$\sum_{m} \langle \psi_{\mathbf{m}} | \, \hat{C} \, | \psi_{\mathbf{m}} \rangle = \sum_{m} \, \operatorname{Tr} \left(\, |\psi_{\mathbf{m}} \rangle \, \langle \psi_{\mathbf{m}} | \, \hat{C} \right) = \operatorname{Tr} \, \sum_{m} \, |\psi_{\mathbf{m}} \rangle \, \langle \psi_{\mathbf{m}} | \, \hat{C} = \operatorname{Tr} \left(\rho_{\mathsf{ave}} \hat{C} \right)$$



Absence of causality : "arrow of time" loses its unique role \rightarrow network of quantum information in space-time

Implementation of space-time duality in 1D





$$\begin{aligned} \mathbf{x} \quad \mathbf{x}$$

Implementation of space-time duality in 1D



1D entanglement phases from 2D shallow quantum circuits







Decoding to overcome the post-selection challenge



In the entangling phase, an initially mixed state (exponential in the system size) purified. In the disentangling phase, initially mixed states are easily purified.

Decoding to overcome the post-selection challenge





Decoding of local order parameter

Cross-correlator \rightarrow "probe" qubit entanglement \rightarrow proxy for the system entanglement



(weak unitary) $\rho \rightarrow 0$



(strong unitary) $\rho \rightarrow 1$





Crossover between entanglement structures

$$S_{\text{proxy}} = -\log_2[(1+\zeta^2)/2]$$



Noise as a probe of the entanglement structure





Coherence \rightarrow upper limit on qubit array sizes of about 12 × 12

TOPOLOGICAL MATTER

Realizing topologically ordered states on a quantum processor



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Satzinger et al., Science 374, pp. 1237-1241 (2021)

Realizing topologically ordered states on a quantum processor



Realizing topologically ordered states on a quantum processor





$$[B_p, A_v] = 0, \ \forall p, v \quad \longrightarrow \quad |G\rangle = \frac{1}{\sqrt{2^{12}}} \prod_p (\mathbb{I} + B_p) \left| 0 \right\rangle^{\otimes 31}$$

superposition of all plaquette configurations



Extracting braiding statistics





Visualizing Dynamics of Charges and Strings in (2+1)D Lattice Gauge Theories



Quantum Al





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External Collaborators













arXiv:2409.17142



Phase diagram of the LGT



E. Fradkin and S. H. Shenker, PRD (1979), S. Trebst et al., PRL(2007), J. Vidal et al., PRB (2009).

Weight Adjustable Loop Ansatz (WALA) ground state



Confinement of electric excitations





Confinement of electric excitations



Dynamics of the string connecting two fixed electric particles



Dynamics of the string connecting two fixed electric particles





Local occupation



Array of coupled non-linear resonators (\rightarrow qubits)



