Contribution ID: 5 Type: **not specified**

Canadian Electronics R&D

Friday 30 January 2015 13:10 (20 minutes)

We will report on progressing testing new signal shaping circuits (designed by Polish group) using 100MHz, 250MHz and 500 MHz digitizers. Will discuss medium term plans for making a full prototype digitizer boards. Will also report on latest progress on RapidIO communications tests.

Length (min.) request (including discussion time)

20

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Presenter: Mr LINDNER, Thomas (TRIUMF)

Session Classification: DAQ and Electronics

Track Classification: DAQ and Electronics